

Notice of References Cited	Application/Control No. 10/091,934		Applicant(s)/Patent Under Reexamination ALON ET AL.	
	Examiner Naum B. Levin		Art Unit 2825	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	C	US-6,453,444 B1	09-2002	Shepard, Kenneth L.	716/2
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NON-PATENT DOCUMENTS

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	U	Goren et al. "An interconnect-aware methodology for analog and mixed signal design, based on high bandwidth (over 40 GHz) on-chip transmission line approach", Design, Automation and Test in Europe Conference and Exhibition, 2002. Proceedings 4-8 March 2002 Page(s): 804 - 811
	V	Tsividis Yannis. "Mixed analog-digital VLSI devices and technology", 1995, pp 1-284
	W	Thor A. Larsen "Mixed analog-digital VLSI devices and technology" By Yannis Tsividis (brief description), World Scientific, 2002, pages 39-40.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.